

U.S. Department of Commerce, Patent and Trademark Office					Atty Docket No.		Serial No.	
					M-5631-1P US		09/837,775	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Applicants			
(Use several sheets if necessary)					Joseph S. Elder et al.			
AUG 13 2002					Filing Date		Group	
					April 17, 2001		2681	
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
16	AA	3,882,424	May 6, 1975	Debois et al.	332	18		
	AB	4,308,508	Dec. 29, 1981	Sommer et al.	332	18		
	AC	4,587,497	May 6, 1986	Keller et al.	331	116 R		
	AD	5,152,005	Sep. 29, 1992	Bickley	455	76		
	AE	5,373,257	Dec. 13, 1994	Shimoda	331	8		
	AF	5,495,512	Feb. 27, 1996	Kovacs et al.	375	376		
	AG	5,612,648	Mar. 18, 1997	McClellan et al.	330	253		
	AH	6,061,550	May 9, 2000	Tanaka et al.	455	91		
	AI							
	AJ							
	AK							
	AL							
	AM							
	AN							
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	AQ							
	AR							
	AS							
Examiner		Date Considered 07/22/03						
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	Joseph S. Elder et al.	8958
	Filing Date	Group
	April 17, 2001	2684

## U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
AA		6,167,246	12/26/2000	Elder et al.	455	313	
AB		6,278,866	8/21/01	Elder et al.	455	192.1	RECEIVED
AC		6,324,390	11/27/01	Elder et al.	455	333	NOV 20 2002
AD		6,377,788	4/23/02	Elder et al.	455	266	

## Foreign Patent Documents

Technology Center 2600

							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AE							

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AF	Abidi, Asad A., "CMOS-only RF and Baseband Circuits for a Monolithic 900 MHz Wireless Transceiver*," Bipolar Circuits & Tech. Mtg., Minneapolis, October 1996, pp. 35-42.
AG	Abidi, Asad A., "Radio-Frequency Integrated Circuits for Portable Communications," <i>Custom IC Conference</i> , San Diego, CA, May 1994, pp. 151-158.
AH	Chang, P. J., Rofougaran, A., and Abidi, A. A., "A CMOS Channel-Select Filter for a Direct-Conversion Wireless Receiver," <i>Symp. On VLSI Circuits</i> , Honolulu, pp. 62-63, 1996.
AI	Chang, J., Abidi, A. A., and Viswanathan, C.R., "Flicker Noise in CMOS Transistors from Subthreshold to Strong Inversion at Various Temperatures," <i>IEEE Trans. On Electron Devices</i> , vol. 41, no. 11, pp. 1965-1971, 1994.
AJ	Khorram, S. et al., "A CMOS Limiting Amplifier & Logarithmic Signal-Strength Indicator," Integrated Circuits & Systems Laboratory, Electrical Engineering Department, University of California, Los Angeles (19 pages).
AK	Rofougaran, A., et al., "A 900 MHz CMOS Frequency-Hopped Spread Spectrum RF Transmitter IC," <i>Custom Integrated Circuits Conf.</i> , San Diego, CA, May 1996, pages 209-212.
AL	Rofougaran, A. et al., "A 900 MHz CMOS Frequency-Hopped Spread Spectrum RF Transmitter IC," Integrated Circuits & Systems Laboratory, Electrical Engineering Department, University of California, Los Angeles (20 pages).
AM	Rofougaran, A., Chang, J. Y.-C., Rofougaran, M., Khorram, S., and Abidi, A. A., "A 1 GHz CMOS RF Front-End IC with Wide Dynamic Range," presented at <i>European Solid-State Circuits Conf.</i> , Lille, France, pp. 250-253, 1995.
AN	Rofougaran, A., Chang, J. Y.-C., Rofougaran, M., Khorram, S., and Abidi, A. A., "A 1 GHz CMOS RF Front-End IC with Wide Dynamic Range," (23 pages).

Examiner	Date Considered
	07/24/03
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	AO						
	AP						
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	AS						
	AT						
Foreign Patent Documents							
							Translation
		Document	Date	Country	Class	Subclass	Yes No
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	AV						
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AS	AX	Rofougaran, A., et al., "A 900 MHz CMOS LC-Oscillator with Quadrature Outputs," presented at the 1996 Int'l. Solid-State Circuits Conference, 10 pages.					
↓	AY	Rudell, Jacques C., University of California, Berkeley, "CMOS VLSI Frequency Translation for Multi-Standard Wireless Communication Transceivers," Qualifying Examination, April 8, 1996, pp. 1-24.					
↓	AZ	Chang, Glenn et al., "An Agile, Low-Power CMOS Digitally Synthesized 0-13 MHz Sinewave Generator," Integrated Circuits & Systems Laboratory, Electrical Engineering Department, University of California, Los Angeles. (14 pages).					
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